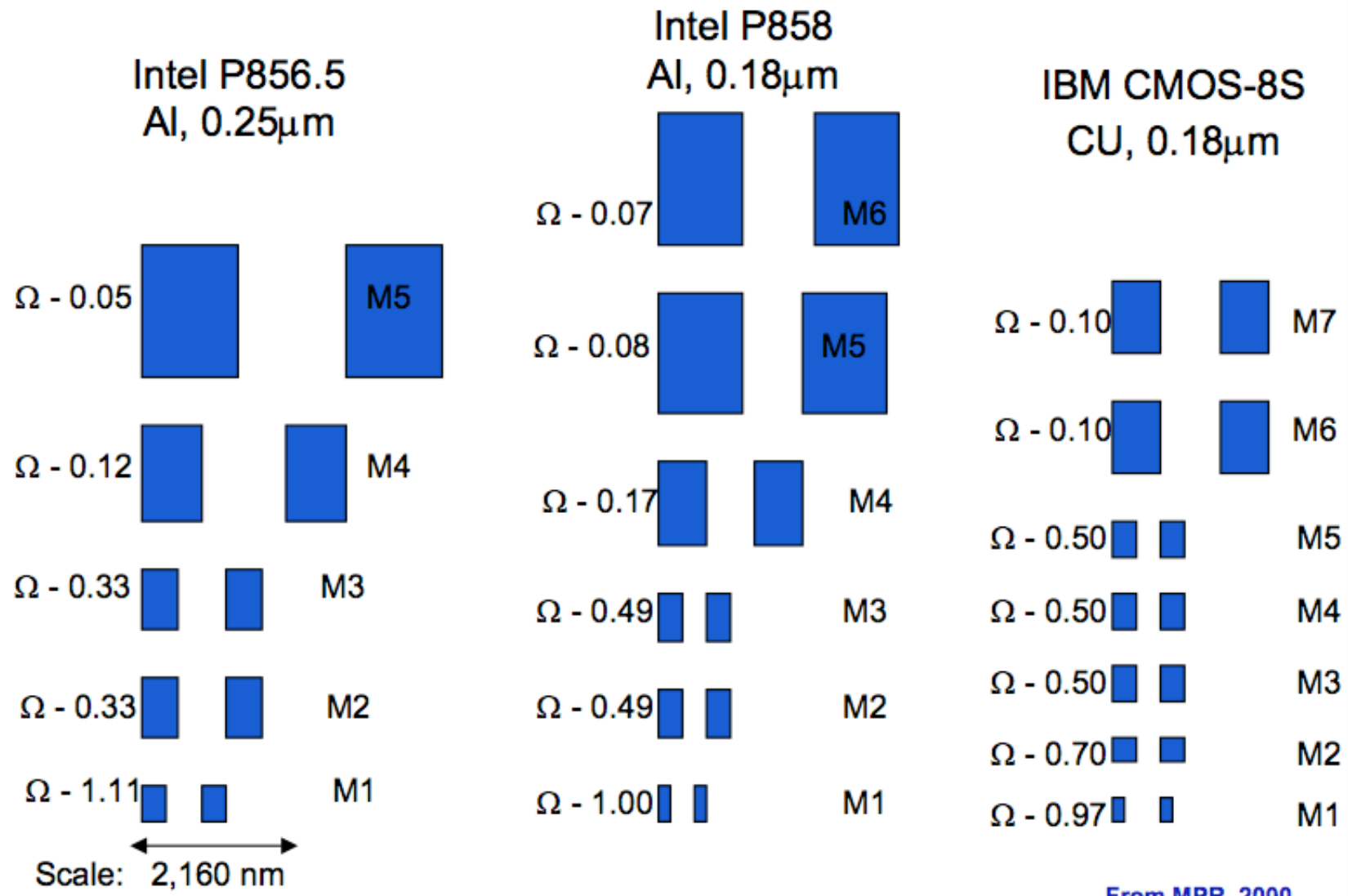




Example geometries

Wire Spacing Comparisons



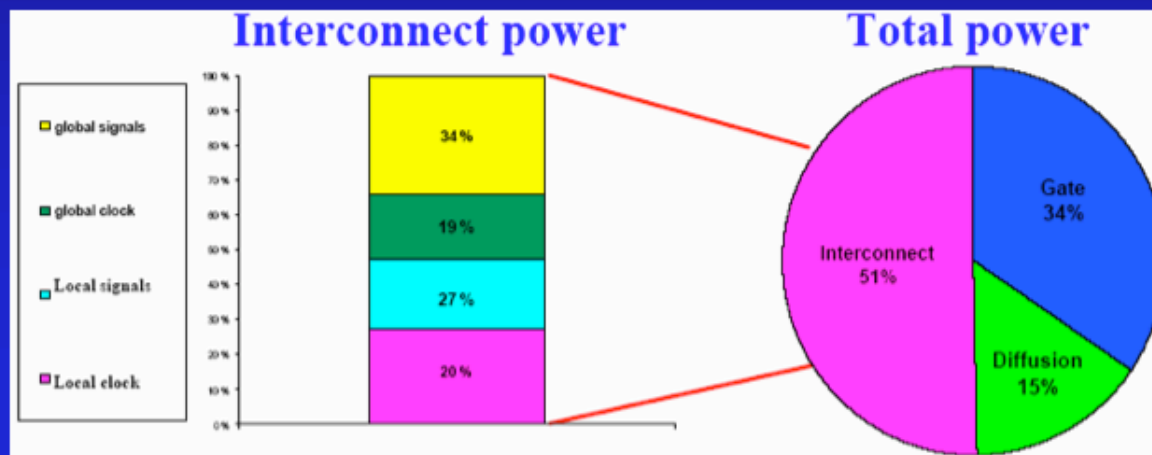
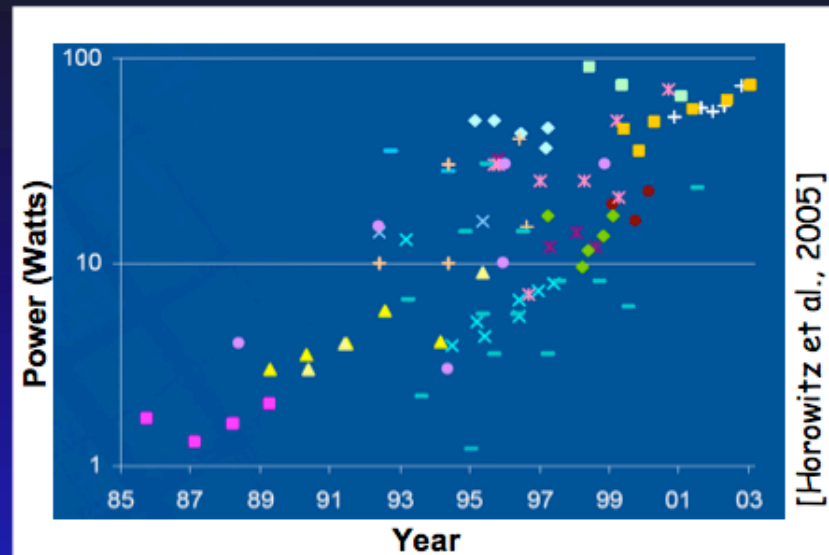
From MPR, 2000



Not only does IC (significantly) contribute to P...

Interconnect Power Dissipation

- Power dissipation is arguably the most critical problem in high-performance chip design
- Over last two decades microprocessor power dissipation grows exponentially and primary contribution from **interconnects**



Interconnect responsible for 50% of dynamic power dissipation [Magen et al., 2004]



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...but it also doesn't scale terrible well.

